



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): DEBASHIS BHATTACHARYA et al.
Serial No.: 09/896,059
For: METHOD FOR AUTOMATED DESIGN OF INTEGRATED
CIRCUITS WITH TARGETED QUALITY OBJECTIVES USING
DYNAMICALLY GENERATED BUILDING BLOCKS
Filed: June 29, 2005
Examiner: Thompson, A. M.
Art Unit: 2825
Confirmation No.: 9475
Customer No.: 27623 Attorney Docket No.: 162.7107USU

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the non-final Office Action dated June 2, 2005, for which the time for response has been extended by an accompanying petition for one month to October 2, 2005, please enter and consider the following amendments and remarks in the above-noted application.

Amendment to the drawing is on page 2.

Amendment to the claims begins on page 3.

Remarks begin on page 17.